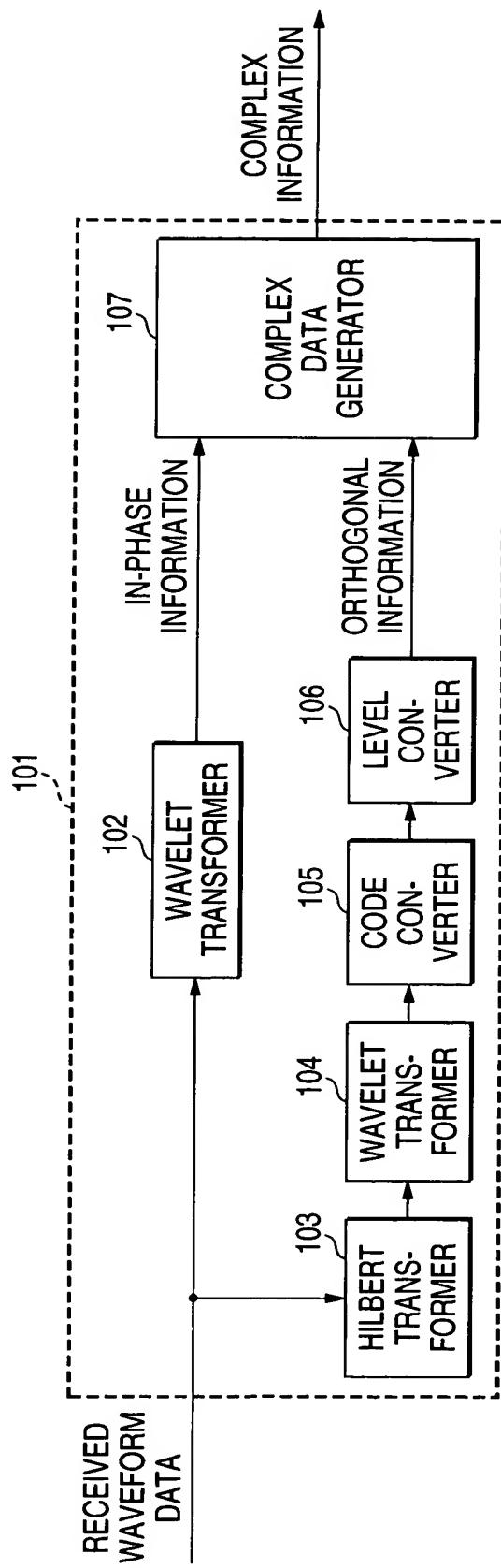




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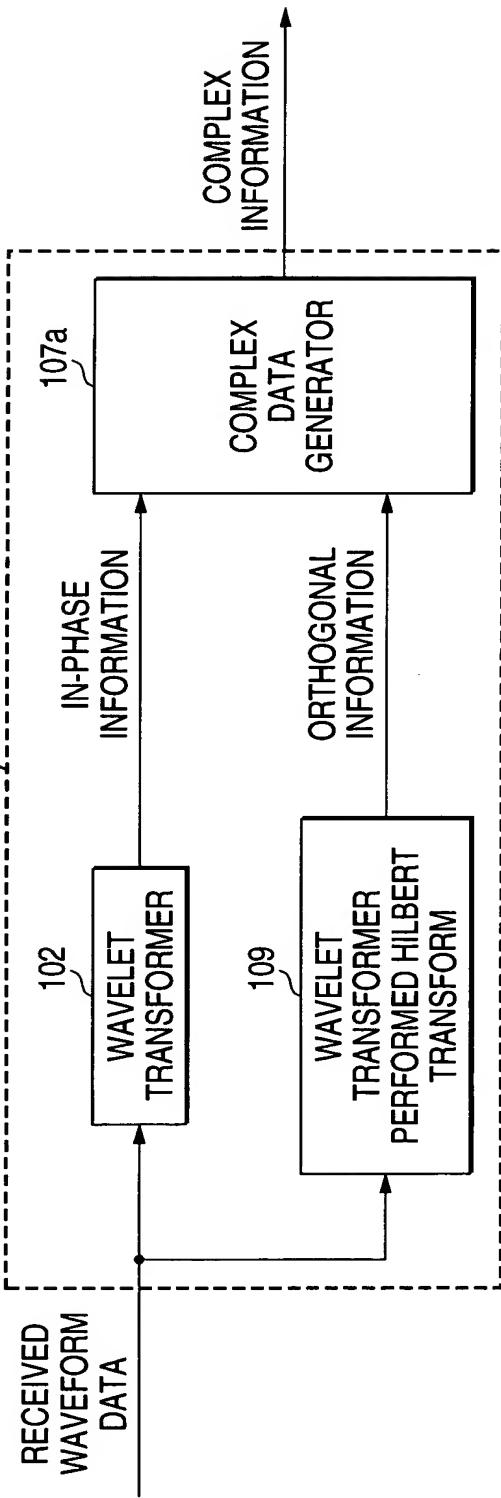
FIG. 1





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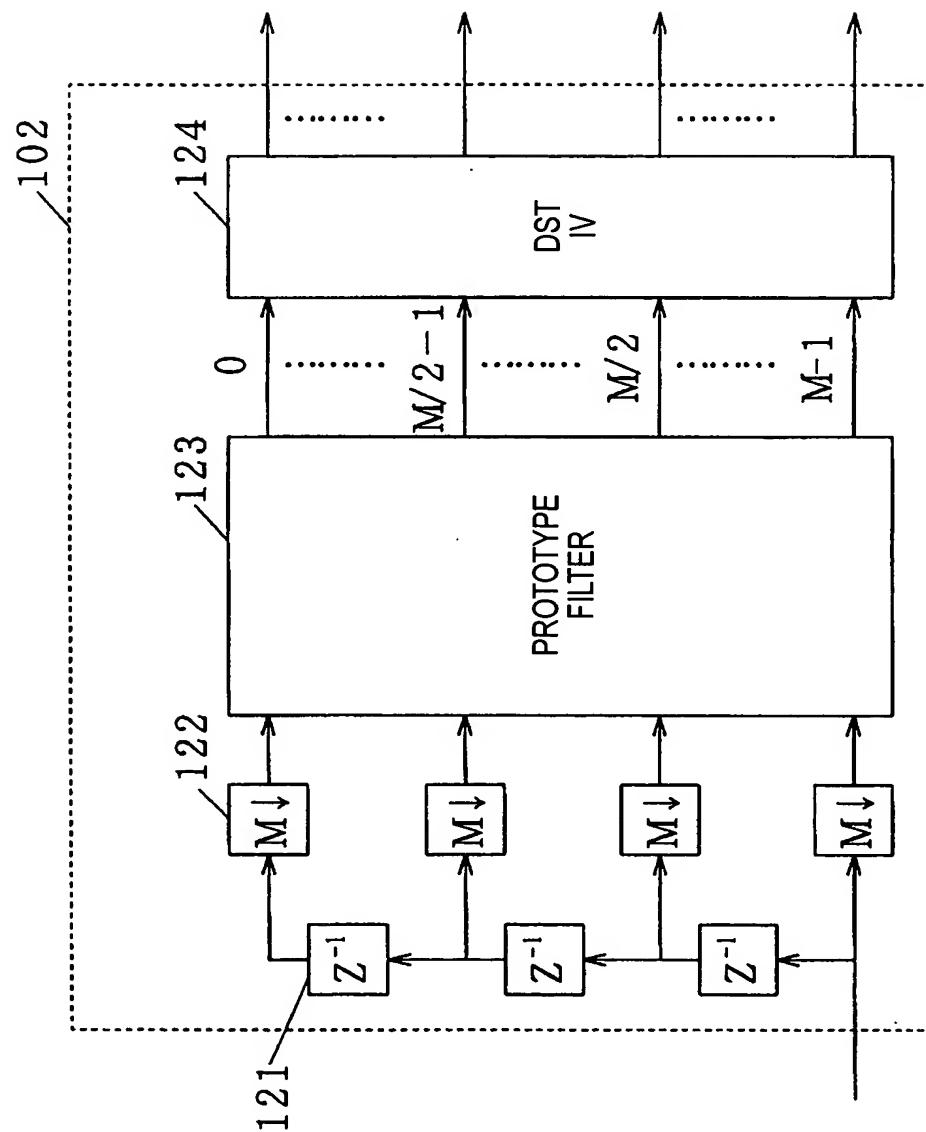
FIG. 2





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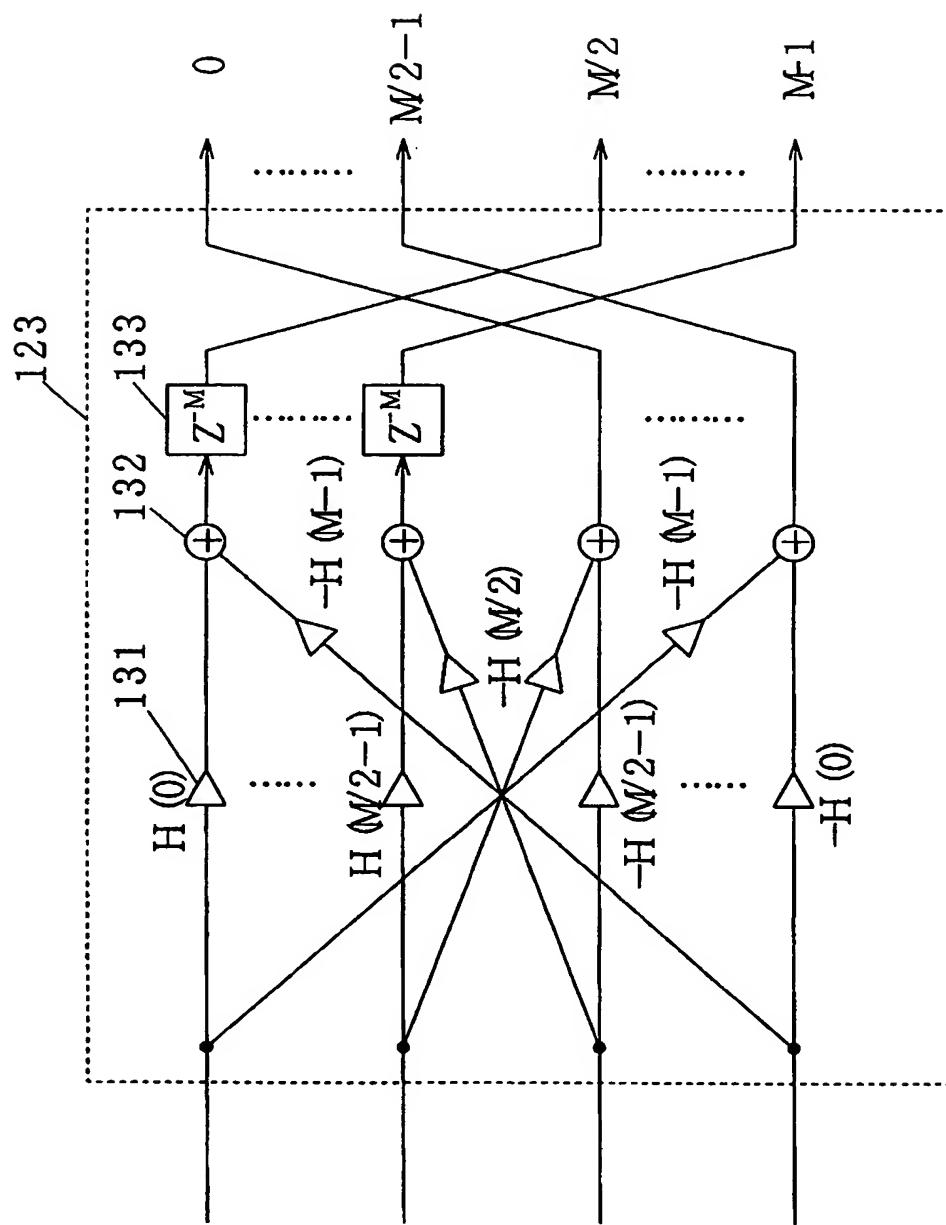
FIG. 3





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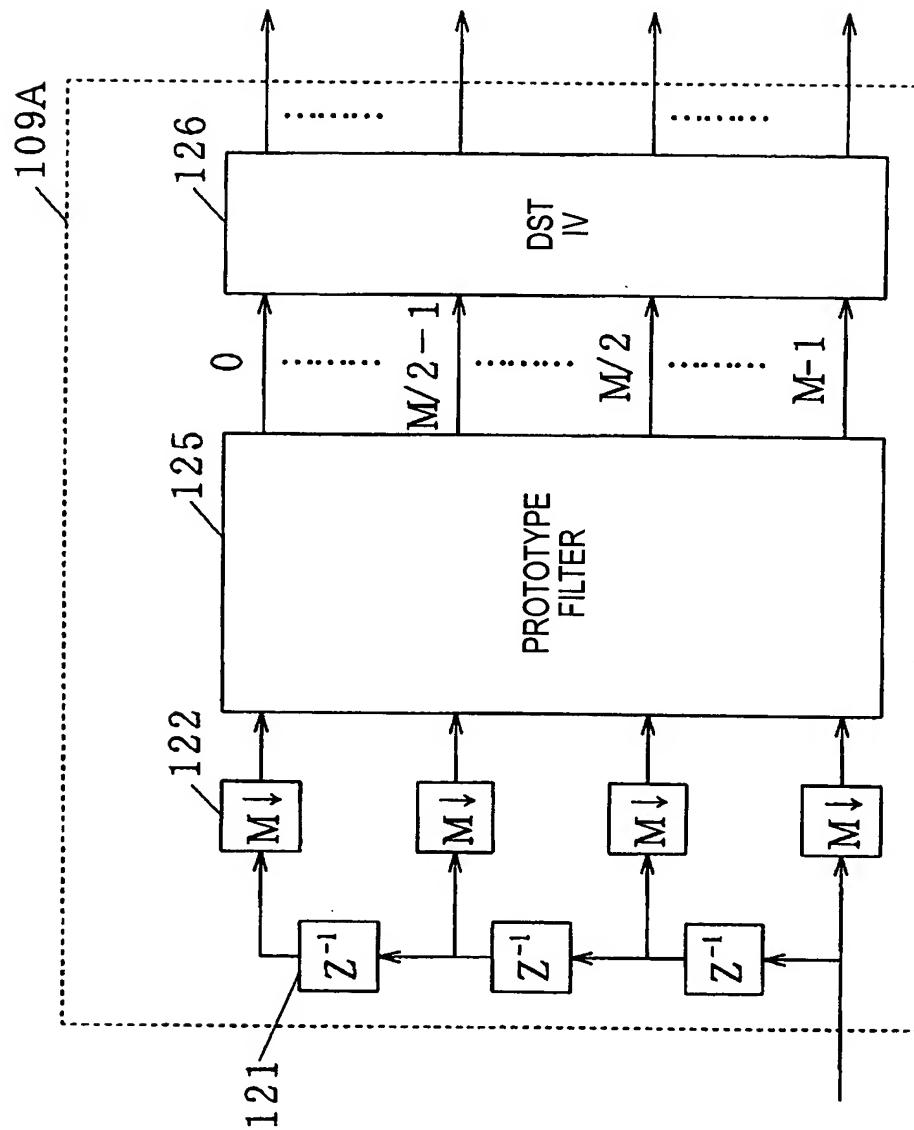
FIG. 4





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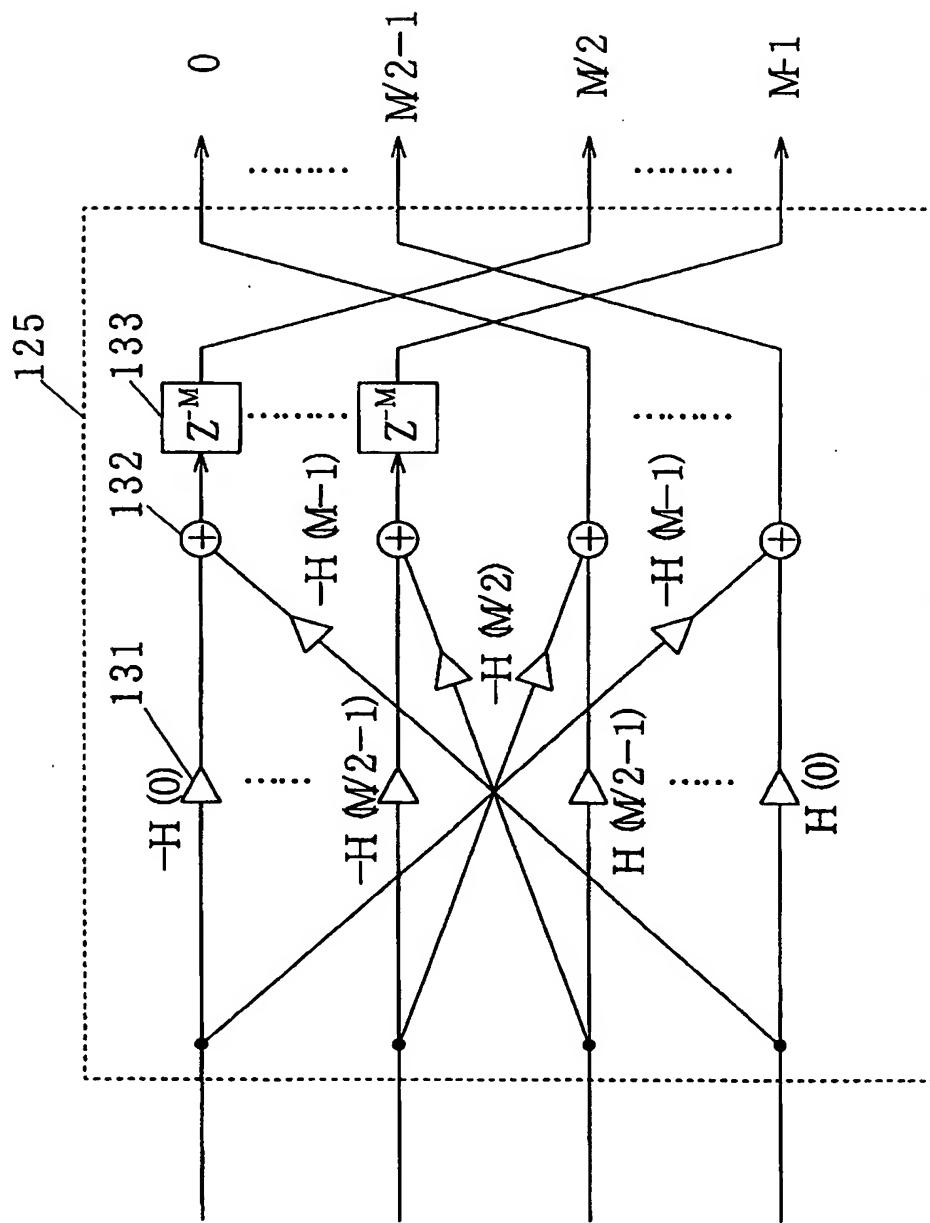
FIG. 5





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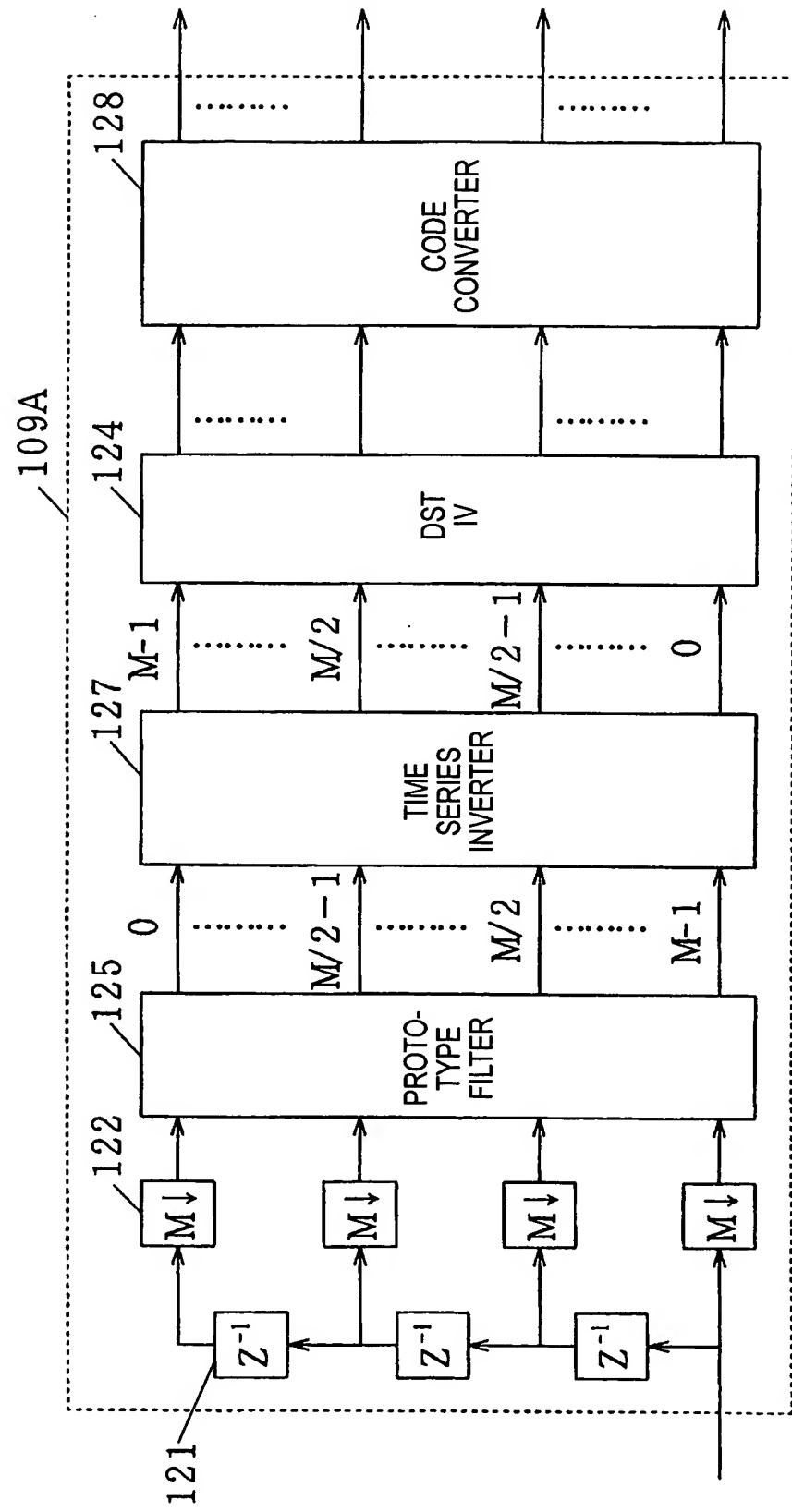
FIG. 6



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FIG. 7





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FIG. 8

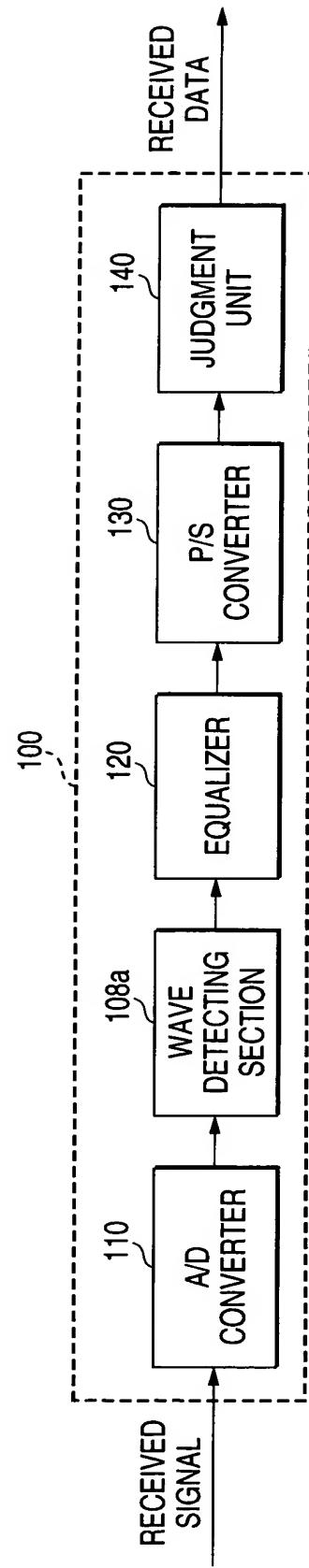




FIG. 9A

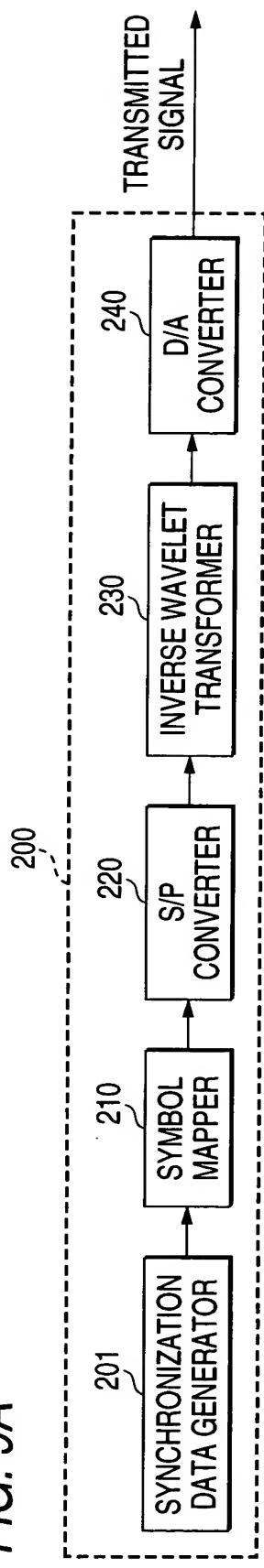
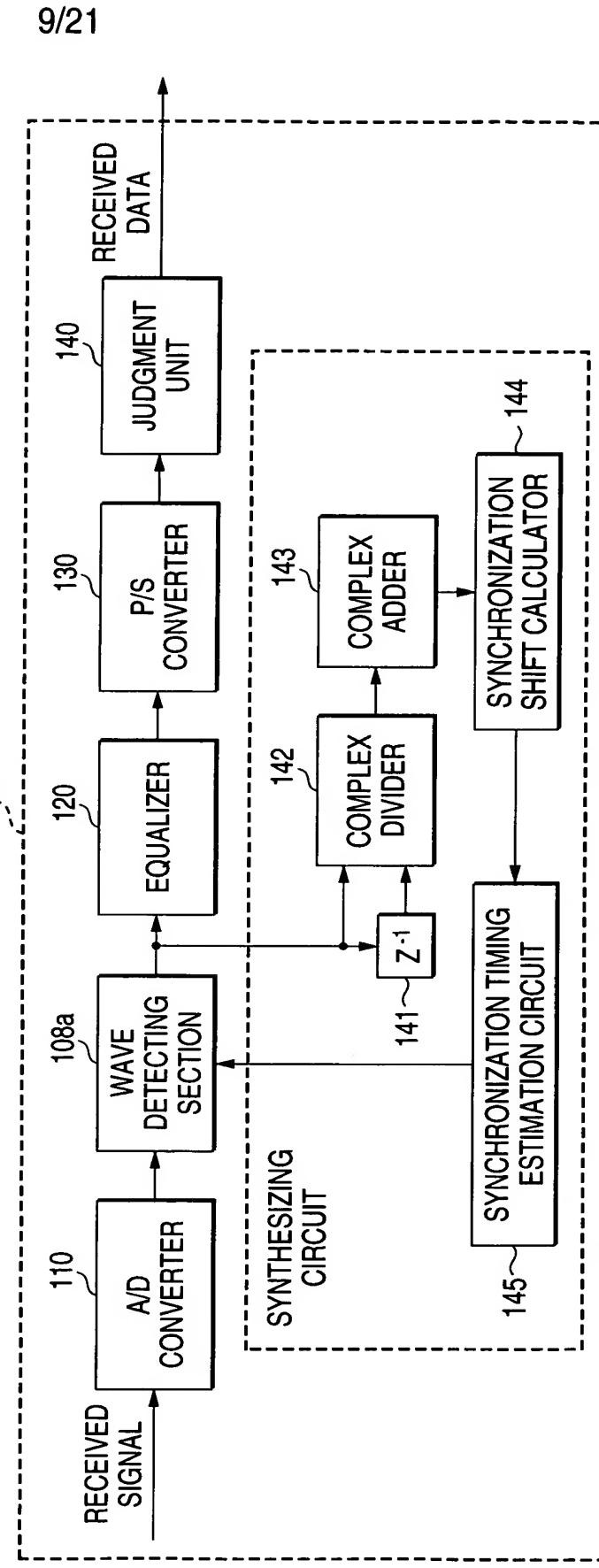


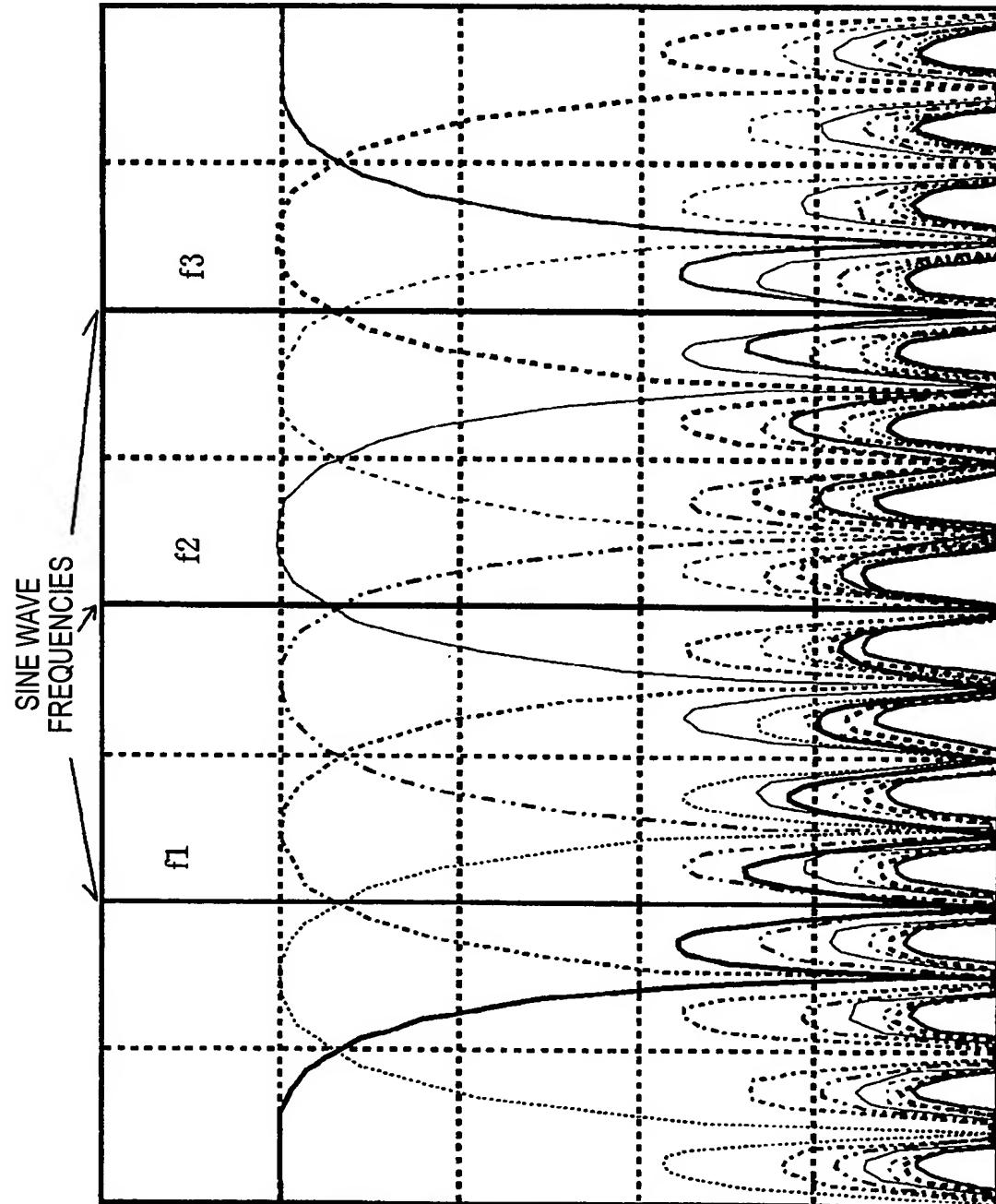
FIG. 9B





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FIG. 10

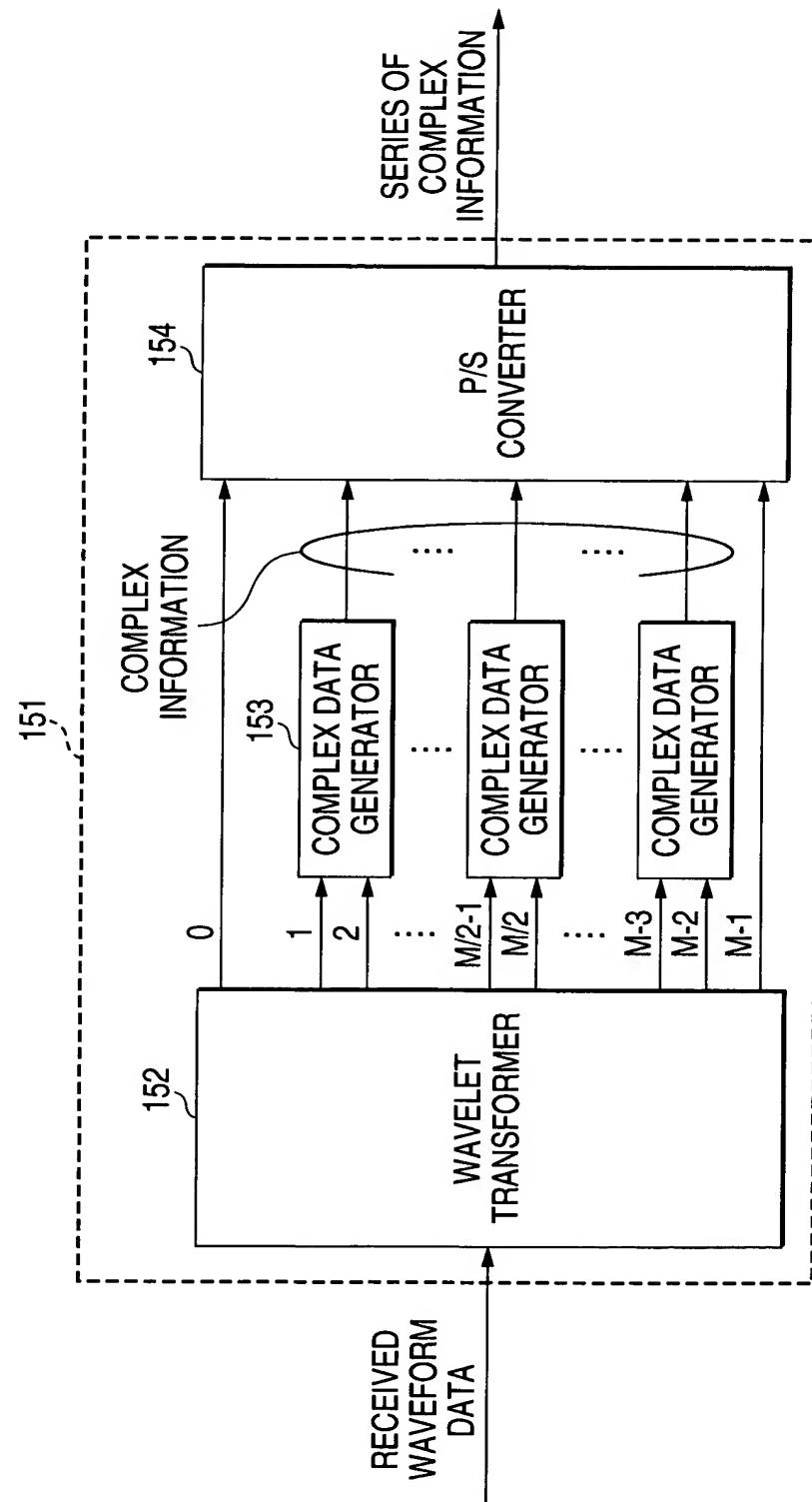


SUBCARRIERS ARE NUMBERED FROM 0 TO 7 STARTING ON LEFT SIDE



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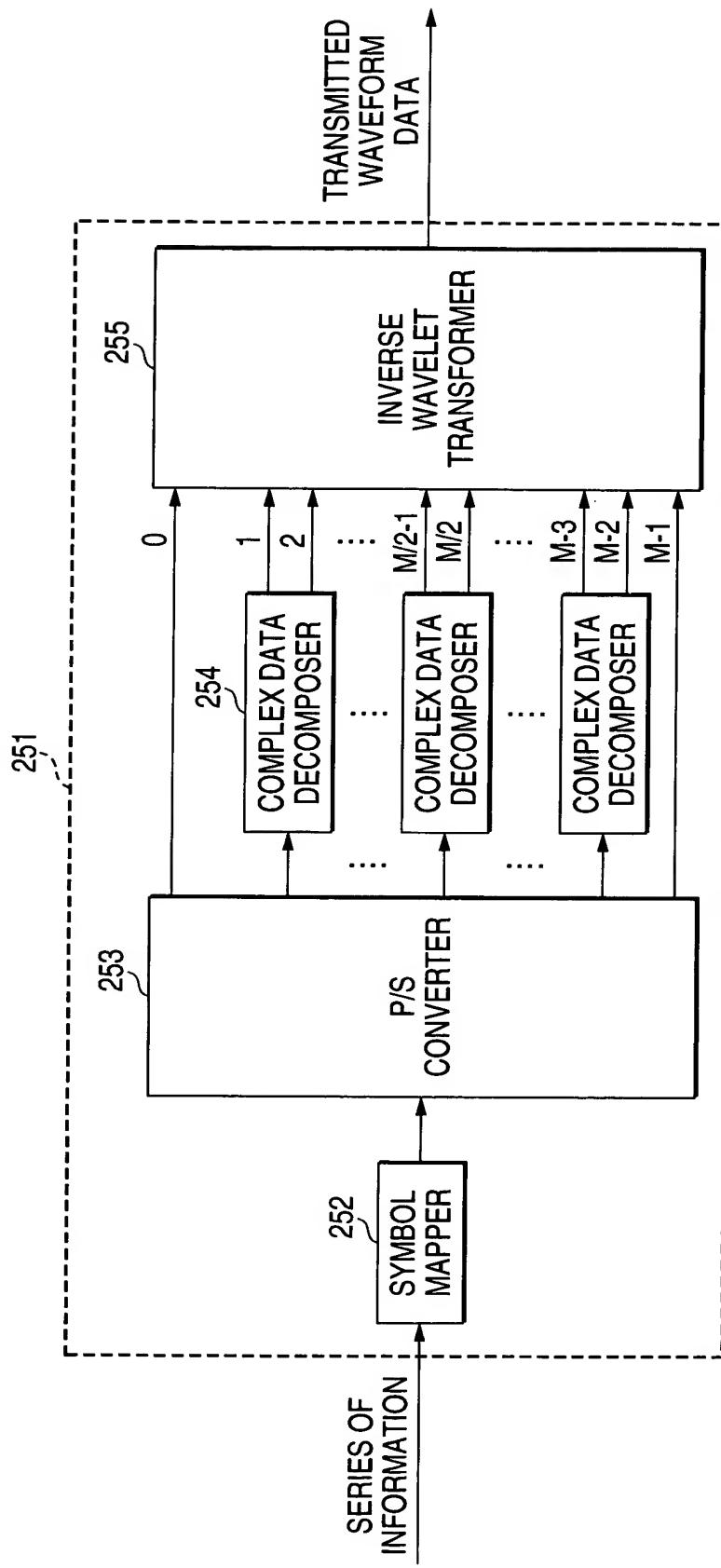
FIG. 11





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FIG. 12





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FIG. 13A

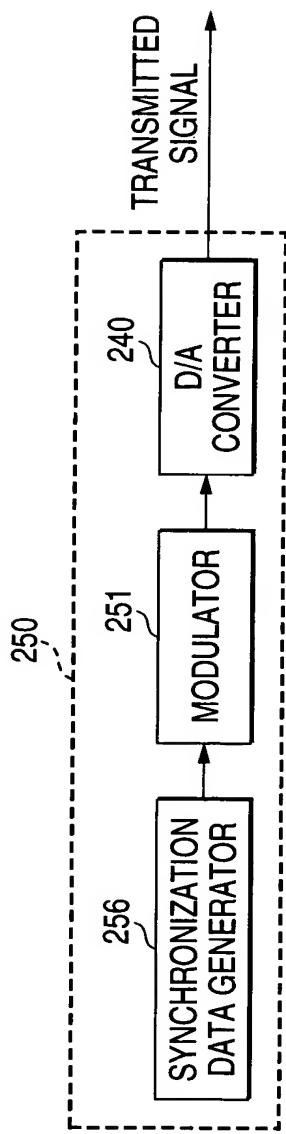
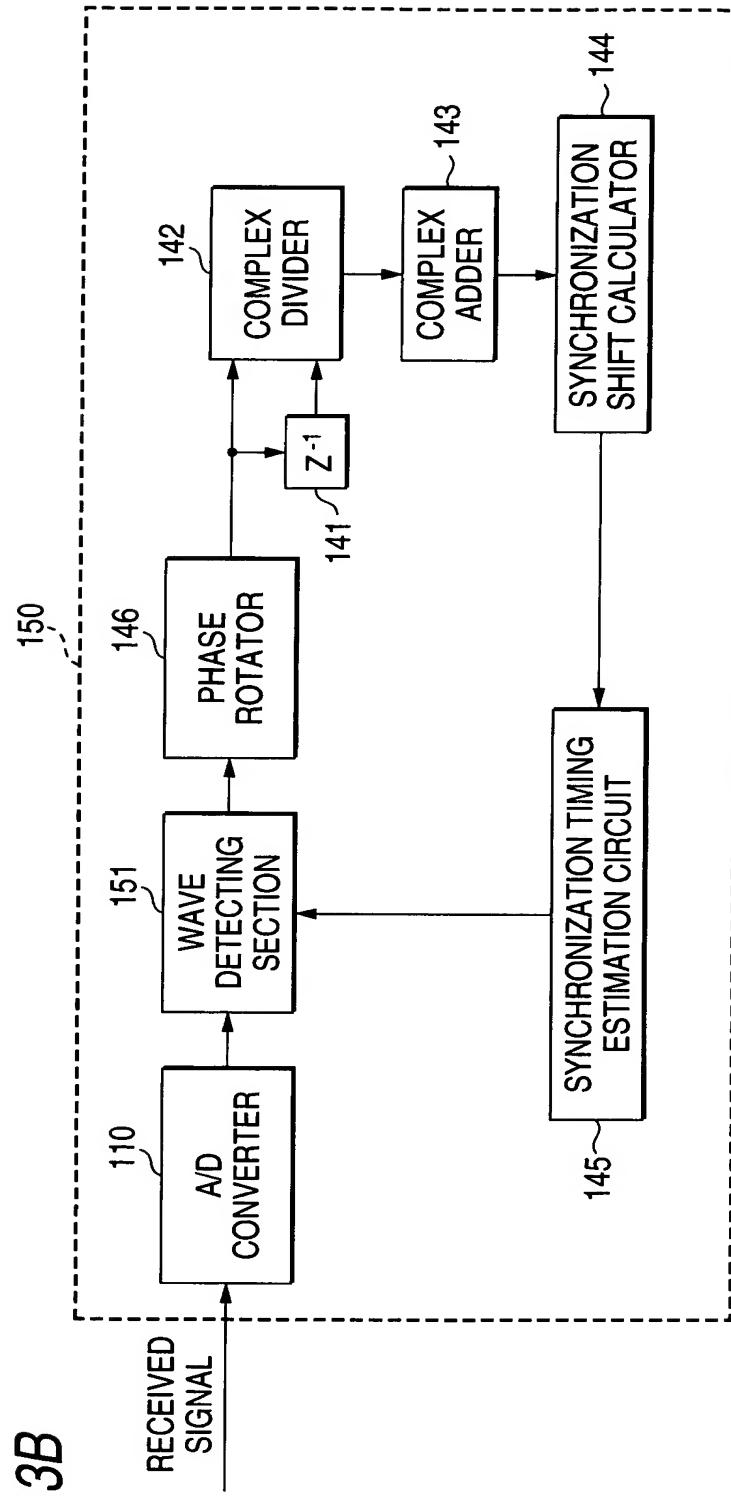


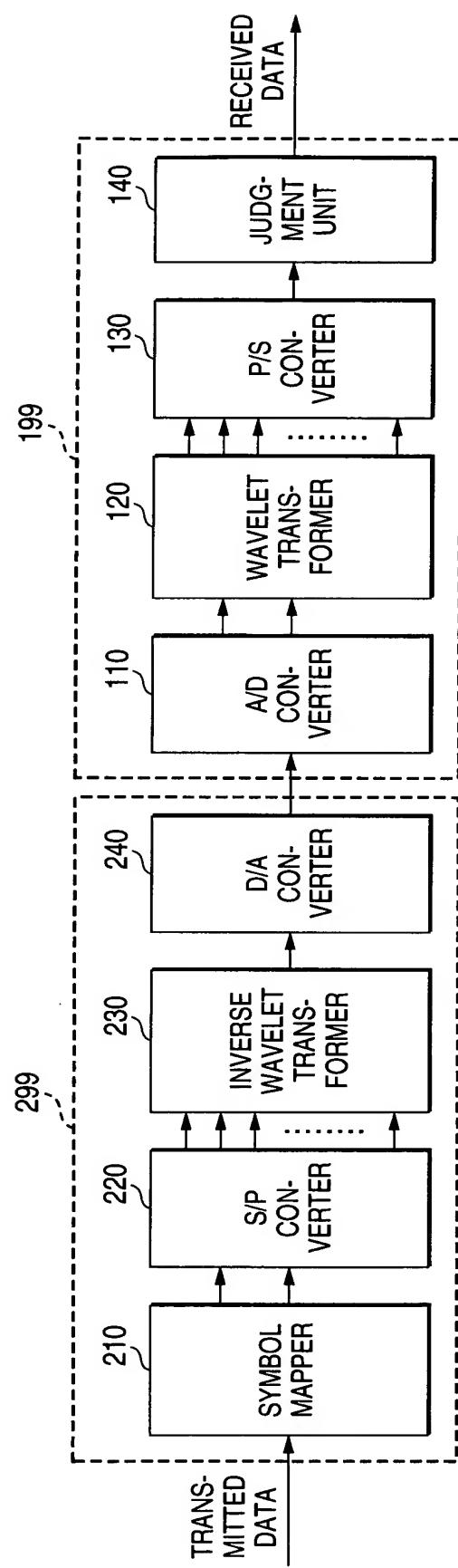
FIG. 13B





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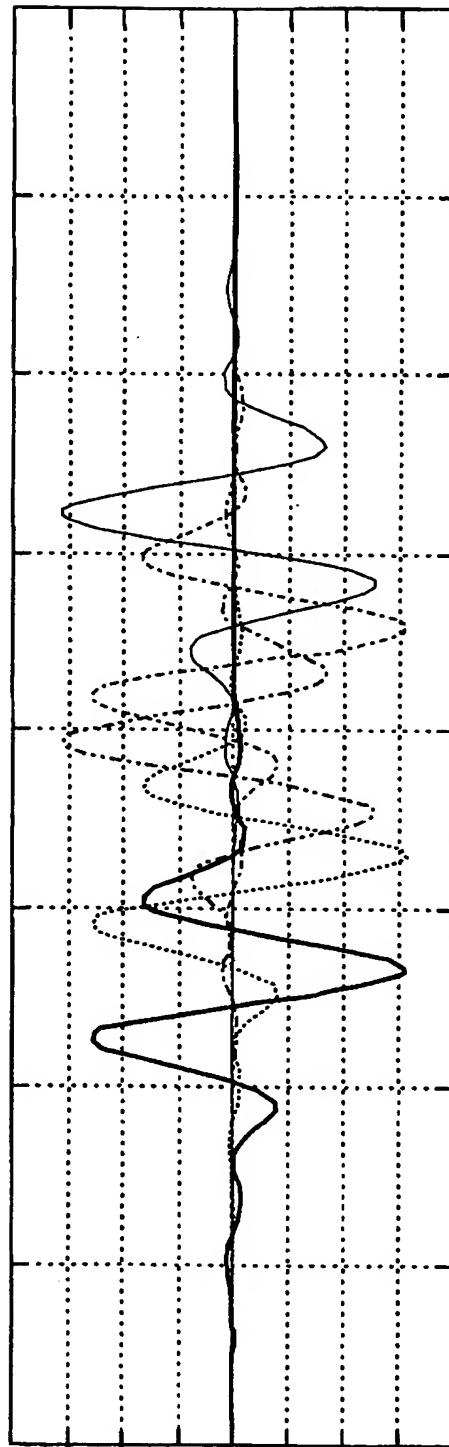
FIG. 14





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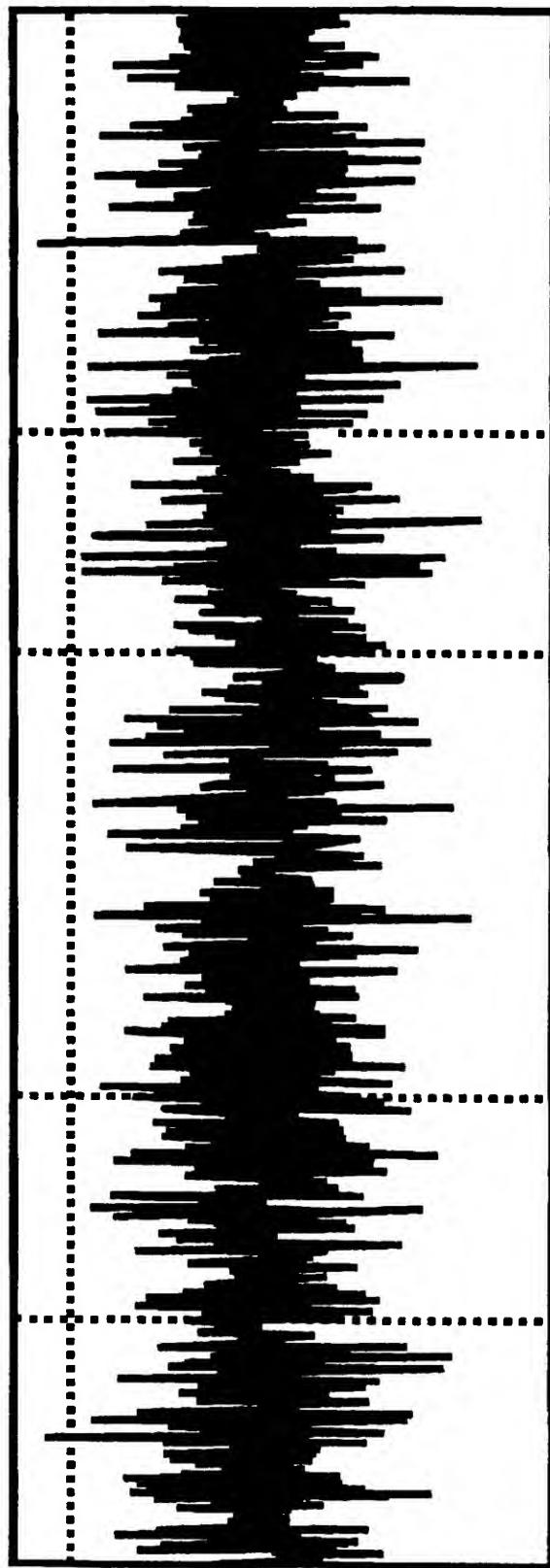
FIG. 15





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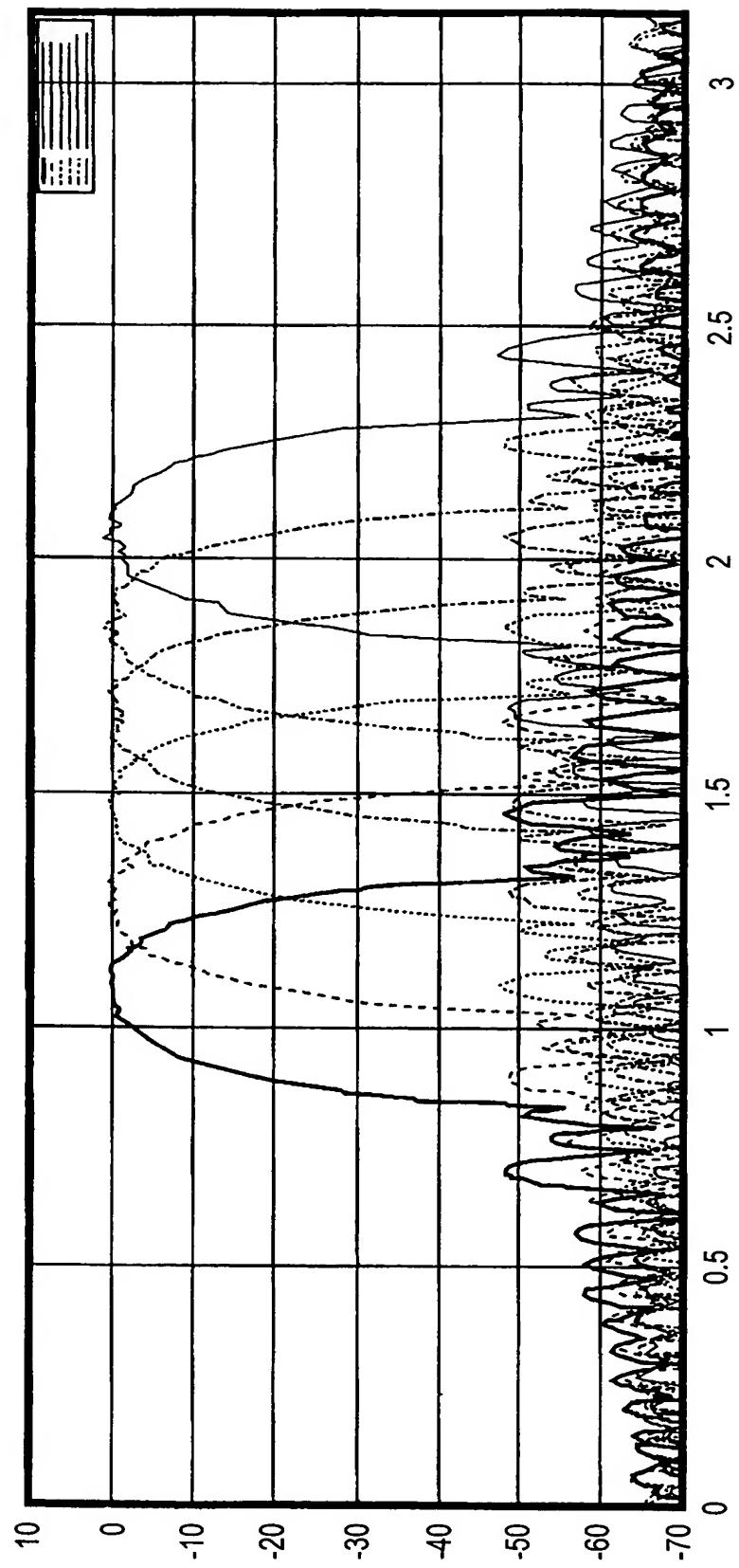
FIG. 16



O I P E
OCT 20 2003
S C I E N C E
M A T H E M A T I C S
C O M P U T E R S

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FIG. 17





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FIG. 18

SYNCHRONIZATION SYMBOL	EQUALIZATION SYMBOL	INFORMATION SYMBOL
------------------------	---------------------	--------------------



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FIG. 19

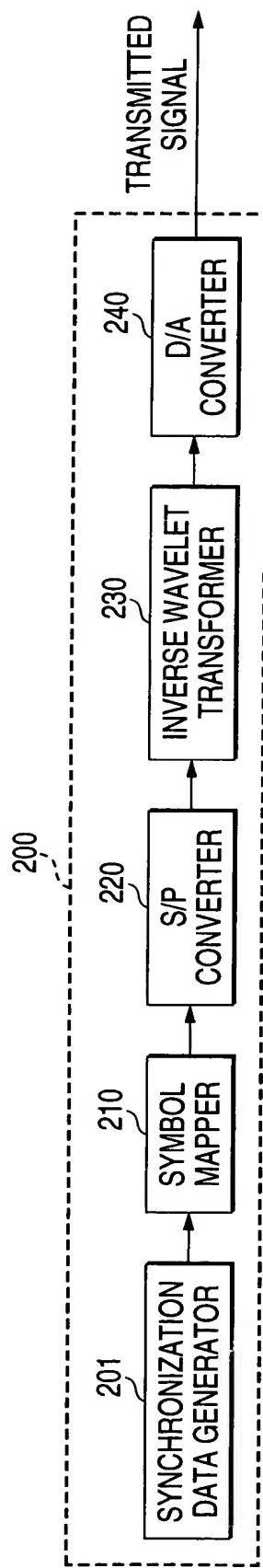


FIG. 20

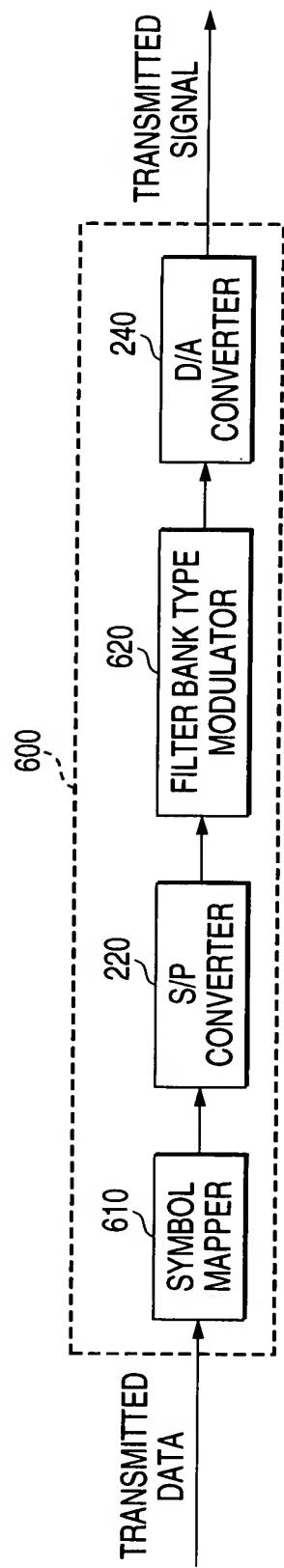
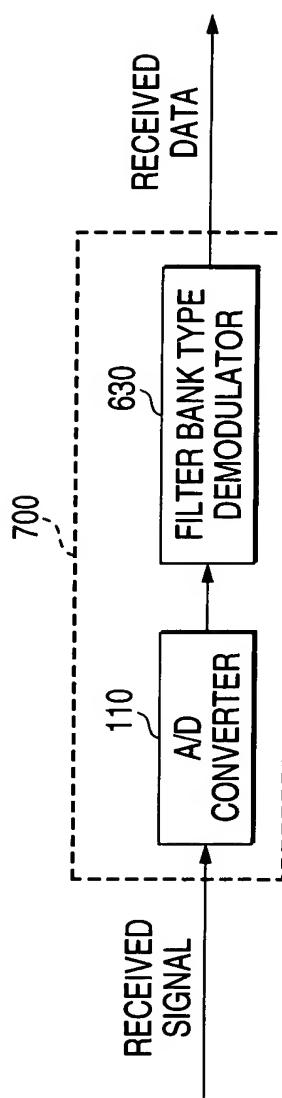


FIG. 21





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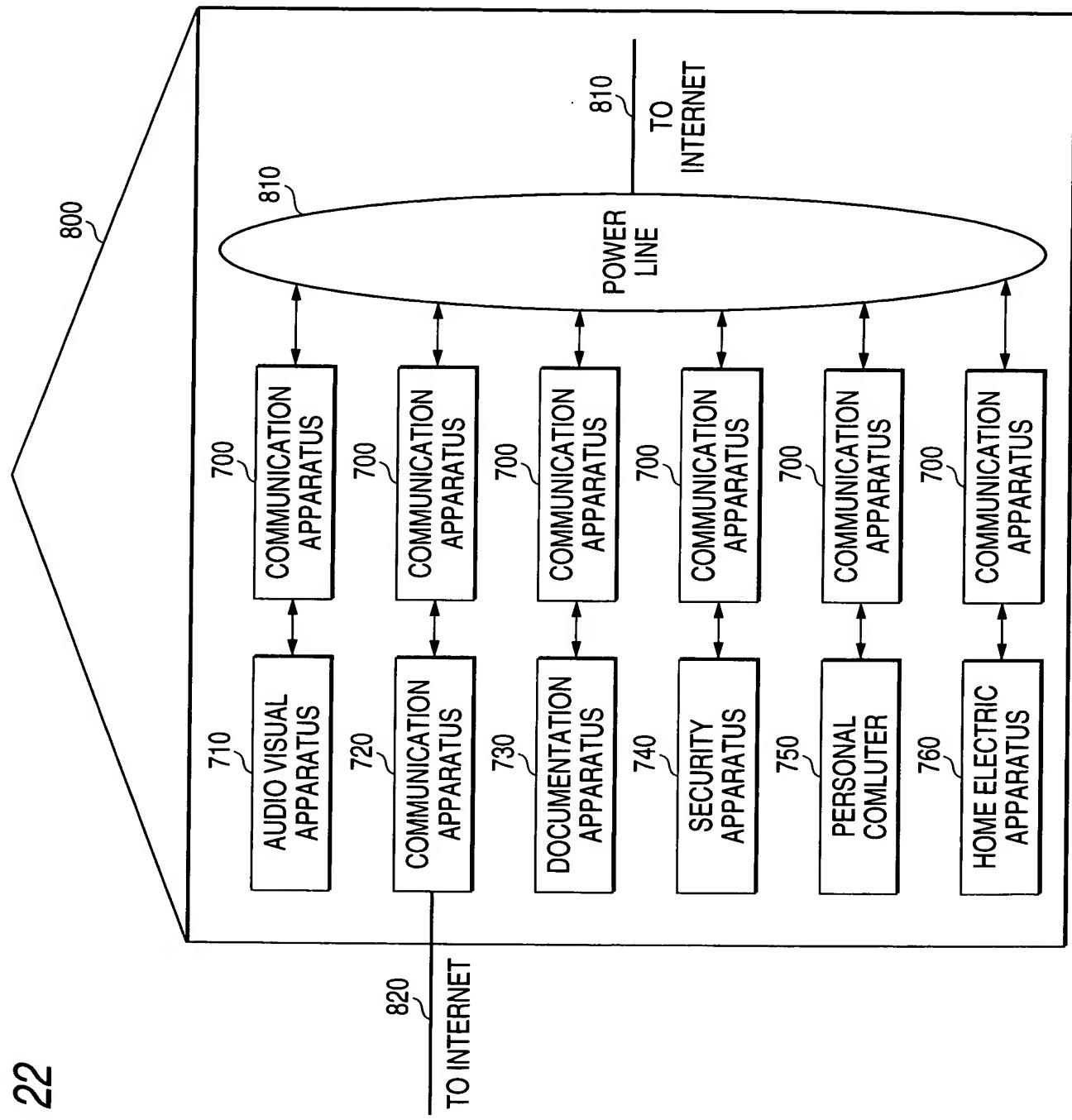


FIG. 22